

	Type	L #	Hits	Search Text	DBs	Time Stamp
1	BRS	L1	0	EIAJ same spice	USPAT	2004/09/27 09:47
2	BRS	L2	24	(Electronics adj Association adj of Japan) same spice	USPAT	2004/09/27 09:48
3	BRS	L3	24	(Electronics adj Industries adj Association adj of Japan) same spice	USPAT	2004/09/27 09:48
4	BRS	L4	1	((Electronics adj Industries adj Association adj of Japan) same spice) and Mosfet	USPAT	2004/09/27 09:49
5	BRS	L5	3	((Electronics adj Industries adj Association adj of Japan) same spice) and semiconductor\$	USPAT	2004/09/27 09:50
6	BRS	L6	2232	((Electronics adj Industries adj Association adj of Japan) same semiconductor\$)	USPAT	2004/09/27 09:50
7	BRS	L7	141146	(Electronics adj Industries adj Association adj of Japan)	USPAT	2004/09/27 09:50
8	BRS	L8	4325	(Electronics adj Industries adj Association adj of Japan) and simulation	USPAT	2004/09/27 09:50
9	BRS	L9	2	(Electronics adj Industries adj Association adj of Japan) and simulation and semiconducts	USPAT	2004/09/27 09:51
10	BRS	L10	0	(EIAJ) and simulation and semiconducts	USPAT	2004/09/27 09:51
11	BRS	L11	323	EIAJ	USPAT	2004/09/27 09:51
12	BRS	L12	0	EIAJ same semiconducts	USPAT	2004/09/27 09:51

	Type	Hits	Search Text
1	BRS	1	5852358.pn.
2	BRS	1	5898321.pn.
3	BRS	1	5995732.pn.
4	BRS	1	5999039.pn.
5	BRS	1	6385565.pn.
6	BRS	9036	(semiconductor same plurality) and (circuit adj board)
7	BRS	130	(semiconductor same plurality) and (circuit adj board) and (simulation same circuit)
8	BRS	18	(semiconductor same plurality) and (circuit adj board) and (simulation same circuit) and inverter and (power adj source\$)
9	BRS	15	(semiconductor same plurality) and (circuit adj board) and (simulation same circuit) and inverter and (power adj source\$) and (Mosfet or Fet)
10	BRS	15	(semiconductor same plurality) and (circuit adj board) and (simulation same circuit) and inverter and (power adj source\$) and Mosfet
11	BRS	15	(semiconductor\$ same plurality) and (circuit adj board) and (simulation same circuit) and inverter and (power adj source\$) and Mosfet
12	BRS	0	( Mosfets\$ same plurality) and (circuit adj board) and (simulation same circuit) and inverter and (power adj source\$) and (signal adj clock)
13	BRS	0	( Mosfet\$ same plurality) and (circuit adj board) and (simulation same circuit) and inverter and (power adj source\$) and (signal adj clock)
14	BRS	0	( fet\$ same plurality) and (circuit adj board) and (simulation same circuit) and inverter and (power adj source\$) and (signal adj clock)
15	BRS	0	( fet same plurality) and (circuit adj board) and (simulation same circuit) and inverter and (power adj source\$) and (signal adj clock)
16	BRS	0	( FET same plurality) and (circuit adj board) and (simulation same circuit) and inverter and (power adj source\$) and (signal adj clock)
17	BRS	365	(power\$ same (integrated adj circuits)) and Spice
18	BRS	267	(power\$ same (integrated adj circuits)) and (Spice same simulation)
19	BRS	57	(power\$ same (integrated adj circuits)) and (Spice same simulation) and (p-channel and n-channel)
20	BRS	3	(power\$ same (integrated adj circuits)) and (Spice same simulation) and (p-channel and n-channel) and EMI